

MILITARY SPECIFICATION

MICROCIRCUITS, LINEAR, CMOS MULTIPLYING D/A CONVERTERS,
MONOLITHIC SILICON

This amendment forms a part of MIL-M-38510/127A, dated 28 February 1986,
and is approved for use by all Departments and Agencies of the Department of Defense.

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Table I, Zero-scale current (I_{ZS} and $I_{ZS'}$), delete and substitute:

Zero-scale current	I_{ZS}	$V_{IL} = 0.8 \text{ V}$ Measured at I_{OUT1} $T_A = +125^\circ\text{C}$	01-06 07	-100 -50	100 50	nA nA
	$I_{ZS'}$	$V_{IH} = 2.4 \text{ V}$ Measured at I_{OUT2} $T_A = +125^\circ\text{C}$	01-06 07	-100 -50	100 50	nA nA

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Table III, tests for I_{ZS} and $I_{ZS'}$, device type 04 only, for $T_A = 125^\circ\text{C}$:
Limits shall be $\pm 100 \text{ nA}$.

Custodians:

Army - ER
Navy - EC
Air Force - 17

Preparing activity:

Air Force - 17

Agent:

DLA - ES

Review activities:

Army - AR, MI
Navy - OS, SH
Air Force - 11, 19, 85, 99
DLA - ES

(Project 5962-0943)

User activities:

Army - SM
Navy - AS, CG, MC

Civil Agency Coordinating Activity:

NASA - NA